		-		<u> </u>	0308
TRANSMITTAL OF FORMAL DRAWINGS				Docket No. BUR9-1999-0235-US1 4-7 (
In Re Application Of: D. Hathaway 12 mg 3/20					
Serial No.	Filing Date	&Batch No.	Examin	ner	Art Unit
	Filing Date	MARKOL			
09/617,908	7/14/00				
Invention: METHO CLOCK TREES	D AND APPARATUS	S FOR MAKING INTEGR	ATED CIRCUIT	S HAVING	GATED
		Address to:			
- Assistant Commissioner for Patents					
		Washington, D.C. 202	31	REC	EIVED
•				JAN 3	3 0 2001
Transmitted herewith	are:				
				OFFICE O	F PETITIONS
29 sheets of formal drawing(s) for this application.					
Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c)					
on the reverse side of the drawing.					
Ho and the transfer of the tra					
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\mathcal{M}	0				
1///1/20	HOLD AS	~	Dated: 12/7/	'00	
UT COVI	Legour		Dated. 12///	<u> </u>	
Mark R. Ferran	Signature				
Reg. No. 46,695					
Schmeiser, Olse	n & Watts				
3 Lear Jet Lane					and attached formal drawings
Latham, NY 121			are being depos		
(518) 220-1850					ass mail under 37 C.F.R. 1.8 at Commissioner for Patents,
			Washington, D.C		
			1/.	$\left(\begin{array}{c} 1 \\ 1 \end{array} \right)$	1 6
			Fym	Mor	lede
			'Signatus	re of Person Ma	illing Correspondence

Kim Dwileski

Typed or Printed Name of Person Mailing Correspondence

1/29 D. HATHAWAY BUR919990235US1 RMK

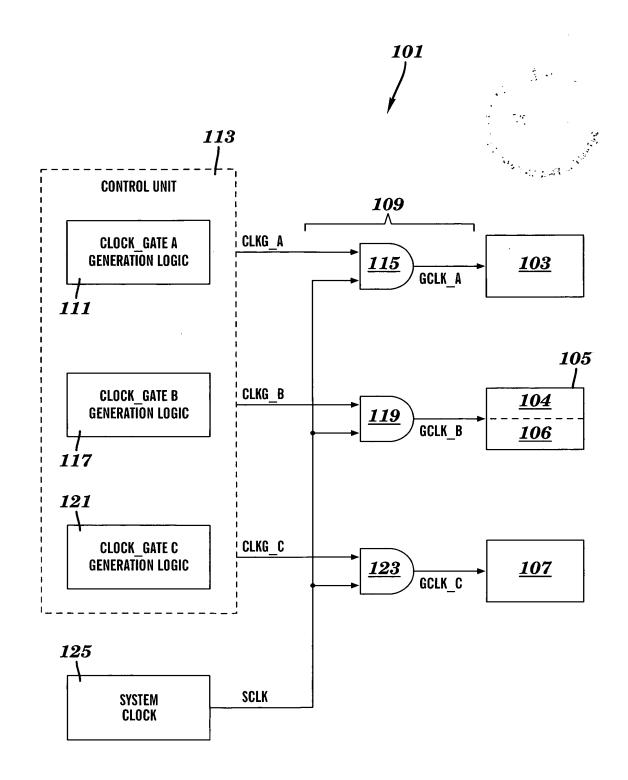


FIG. 1a

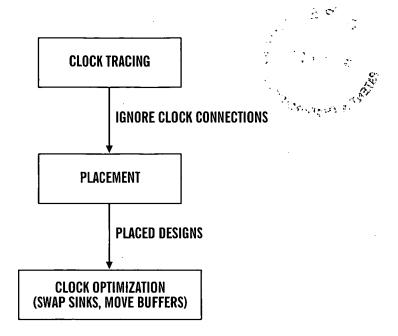


FIG. 1b

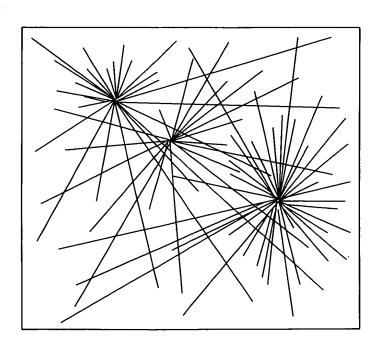


FIG. 1c

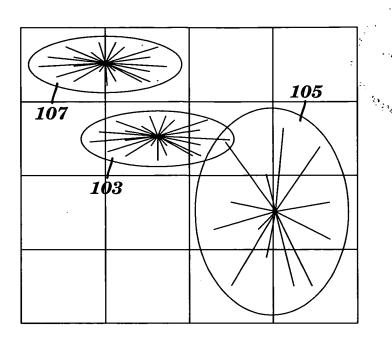


FIG. 2a-i

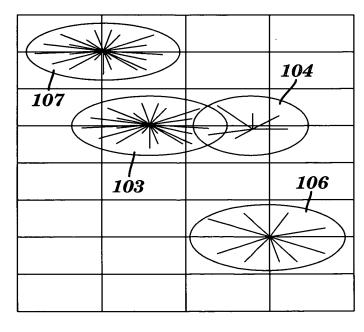


FIG. 2a-ii

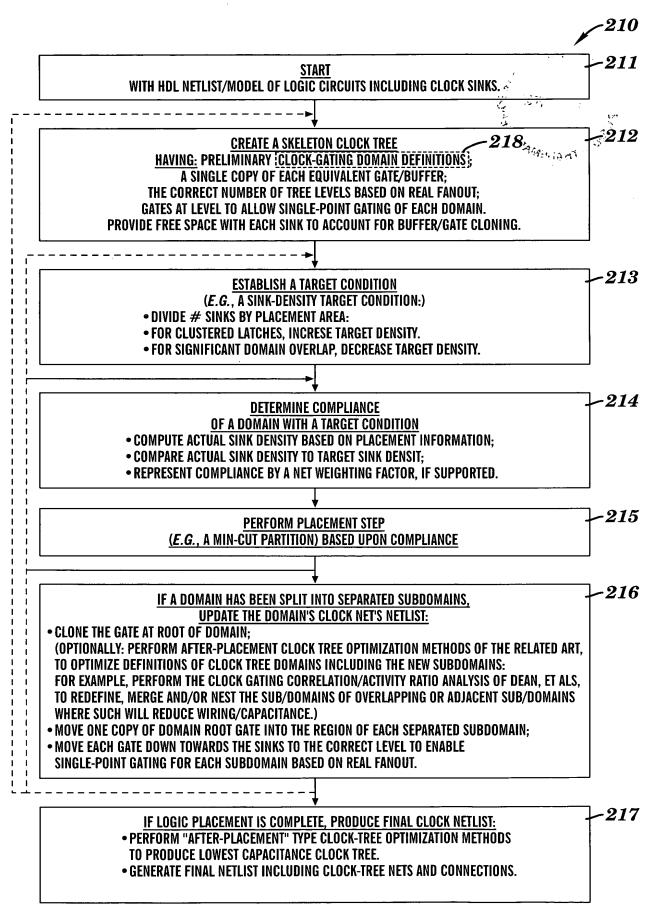


FIG. 2b

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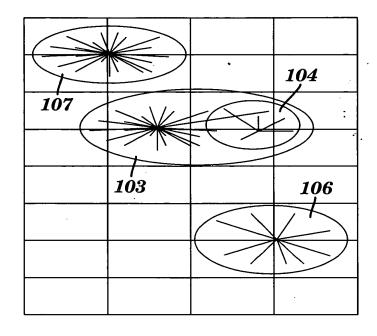


FIG. 2d

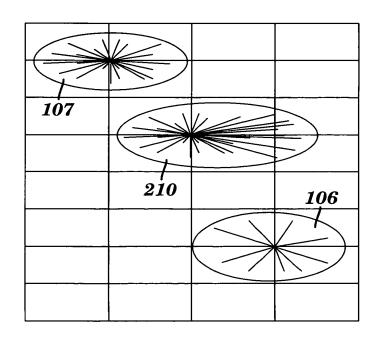
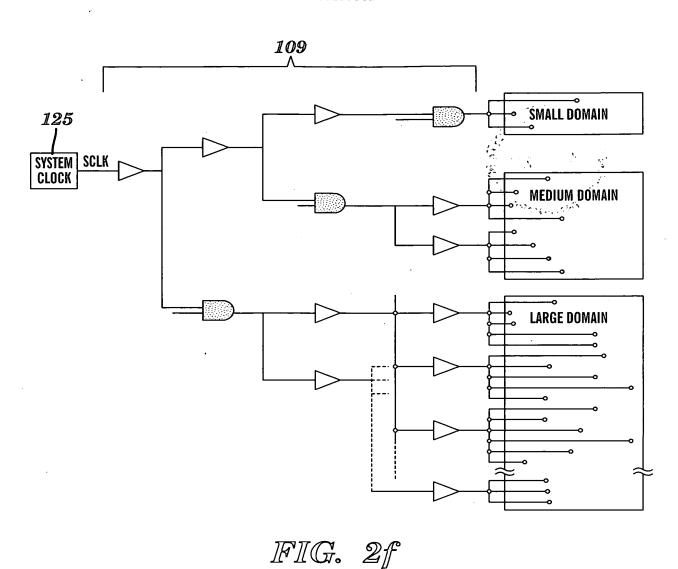


FIG. 2e



SYSTEM SCLK CLOCK MEDIUM DOMAIN

FIG. 28

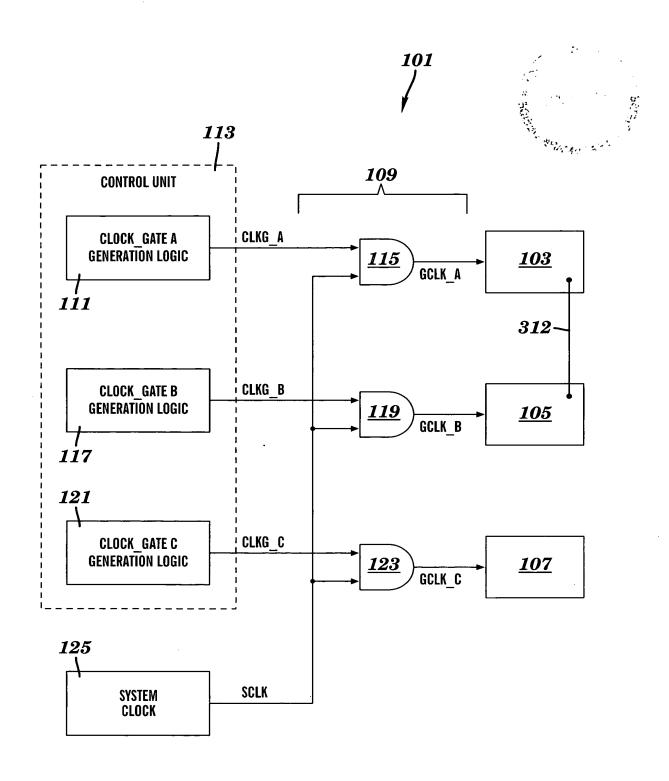


FIG. 3a

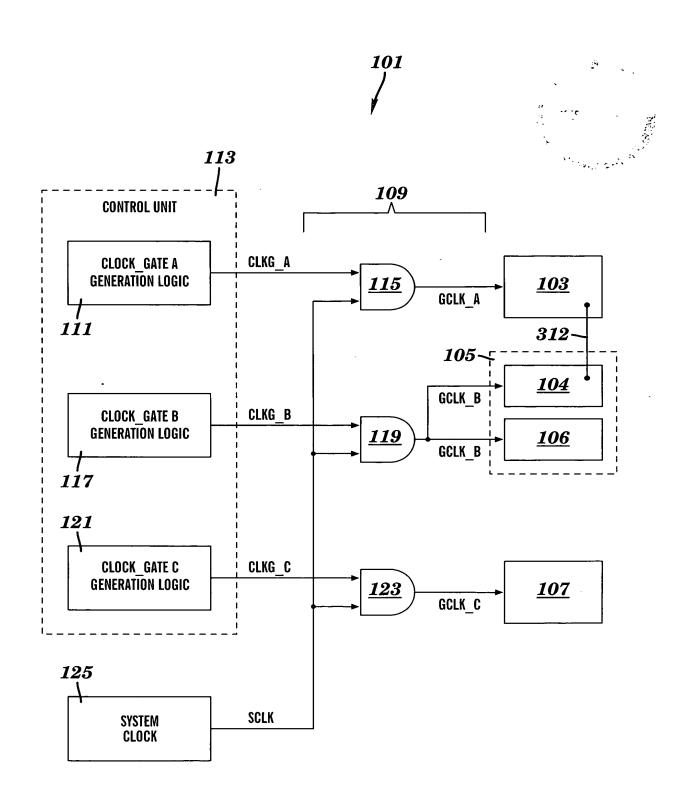


FIG. 3b

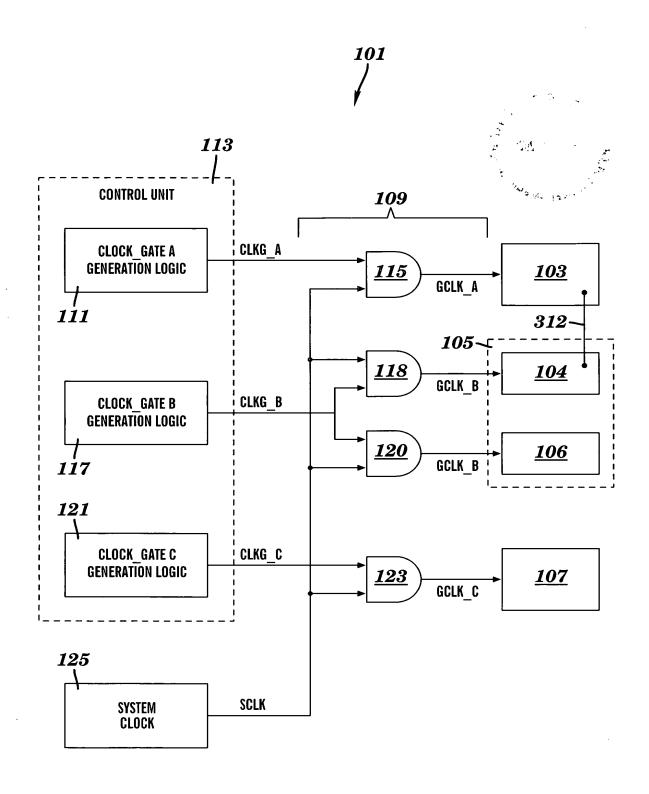


FIG. 3c

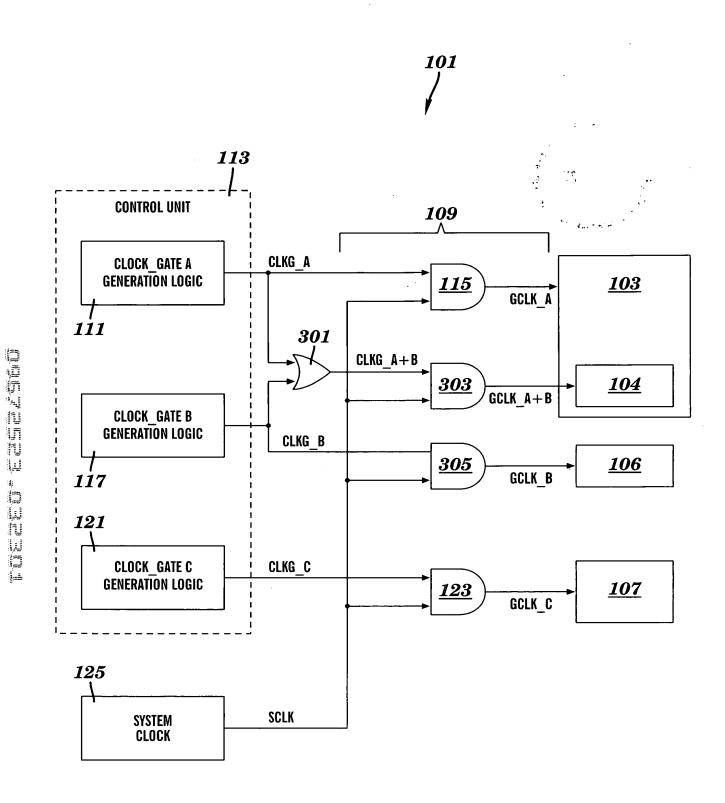


FIG. 3d

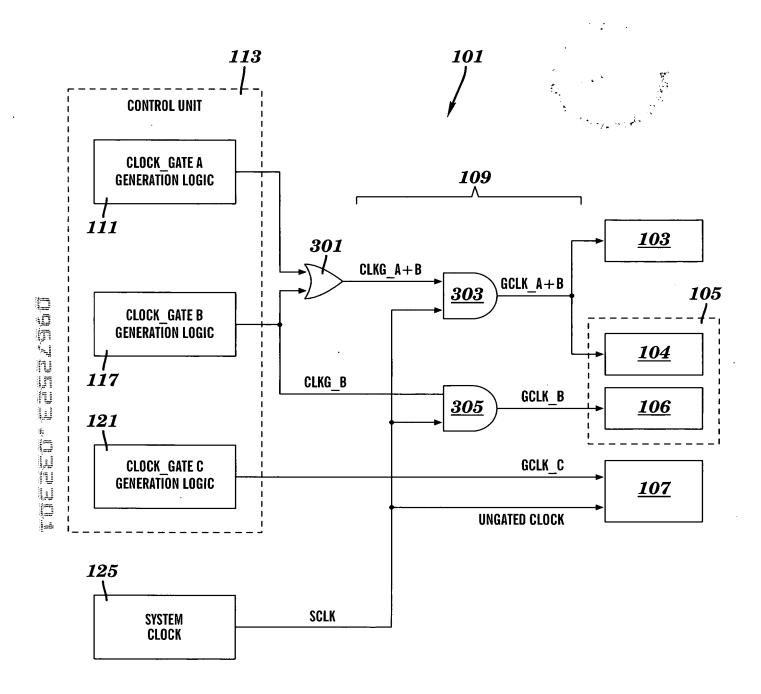


FIG. 3e

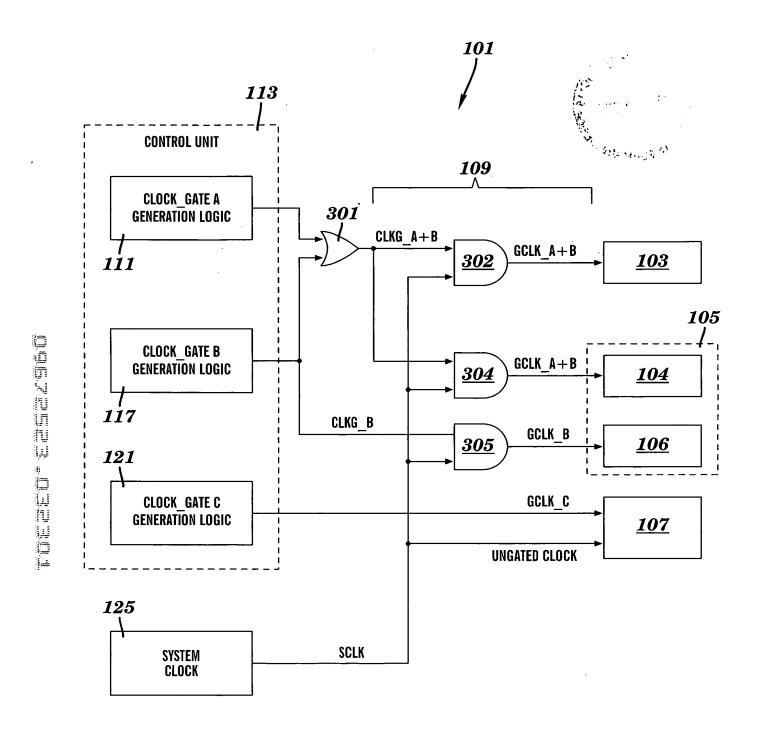


FIG. 3f

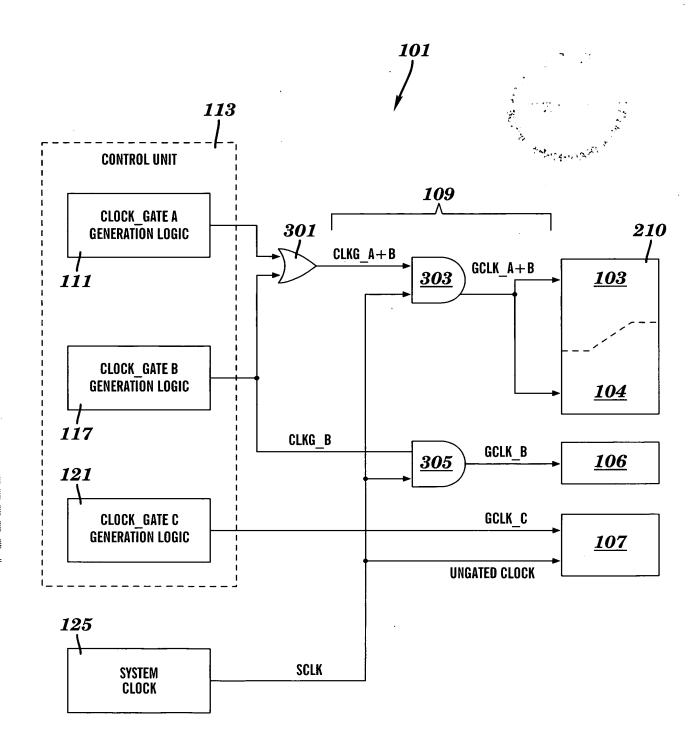


FIG. 3g

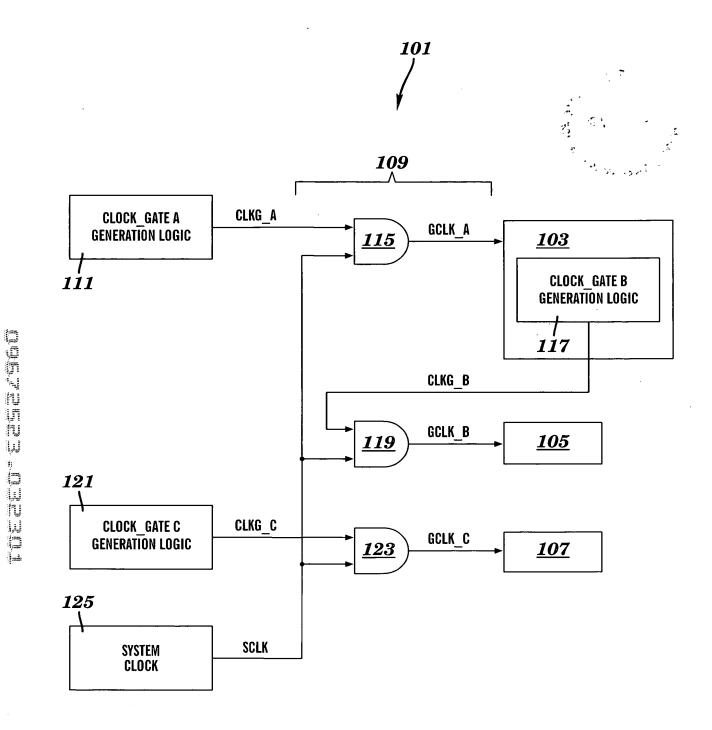


FIG. 3h

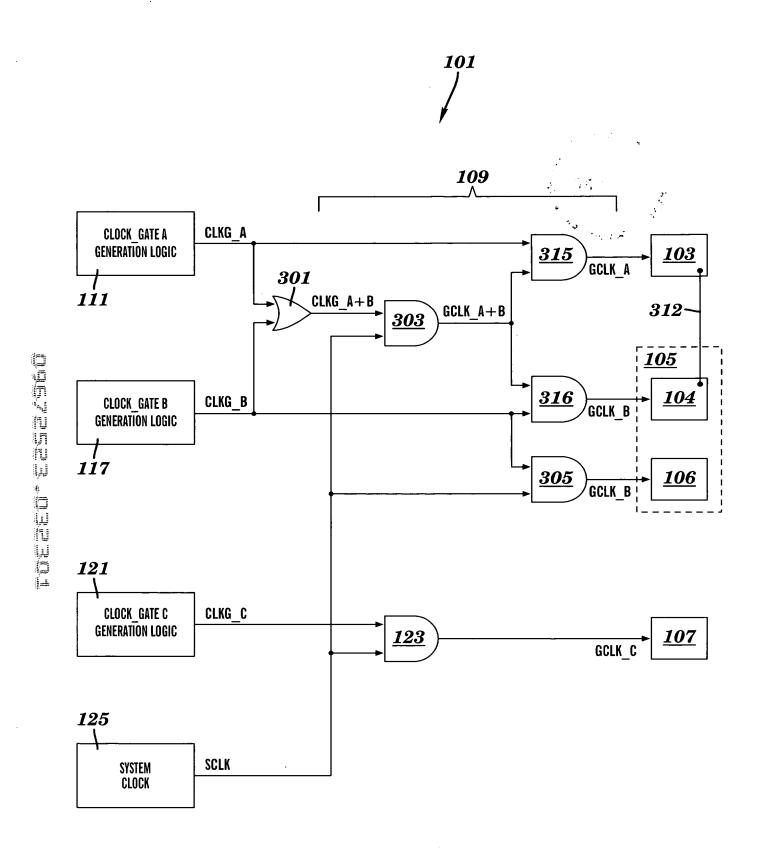
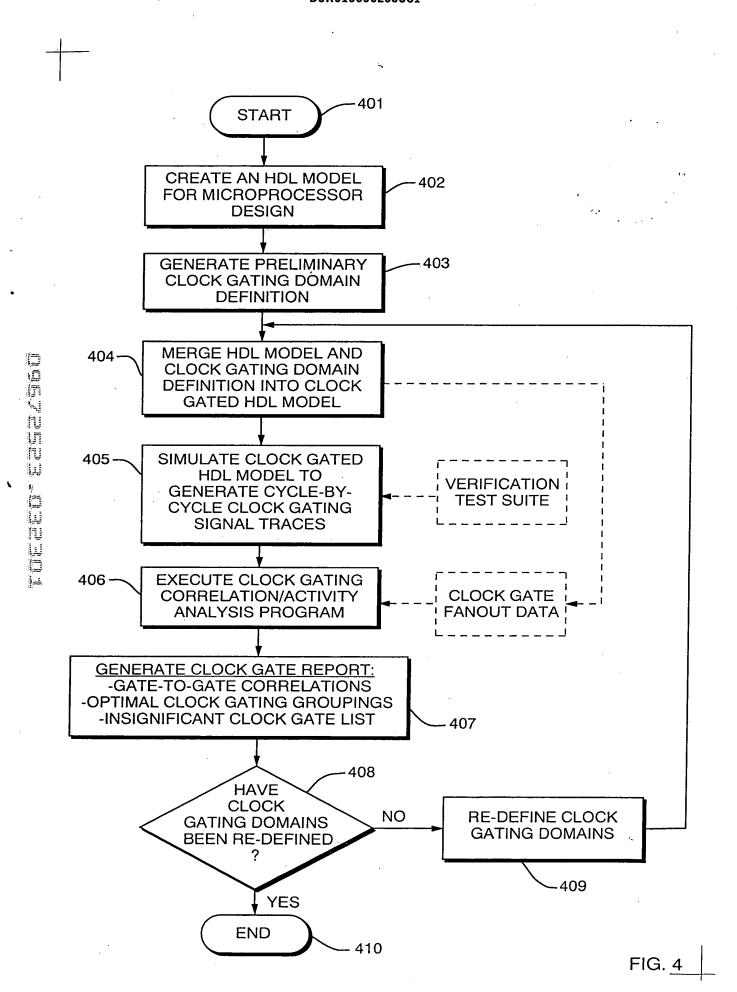


FIG. 3i



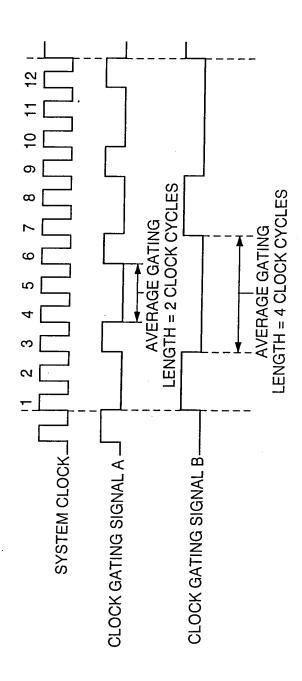


FIG. 5

605

ClockGate Analysis

Calculated number of clockgates: 3
57 Vectors analyzed
Writing activity file clock groups.aet

ClockGate Report Usefullness Activity %Latches ((1-activity) * (%Latches*100)) Clock CLKG A 0.316 0.40 27.4 CLKG B 0.316 0.50 34.2 CLKG C 0.947 0.10 0.5

Useless CLOCKGATES as defined by threshold: 5

Signal CLKG_C has a usefullness metric of 0.5

Writing Correlation File clock_groups.cor

Correlated CLOCKGATE Groups at threshold: 0.9

GROUP 1 Correlation=

CLKG_A: activity 0.316

CLKG_B: activity 0.316

CLOCK_GROUPS.COR file

Signal Correlation Report

C C C C L L K K K G G G

CLKG_A 1.00 0.95 0.37 CLKG_B 0.95 1.00 0.37 609 CLKG_C 0.37 0.37 1.00

FIG. 6

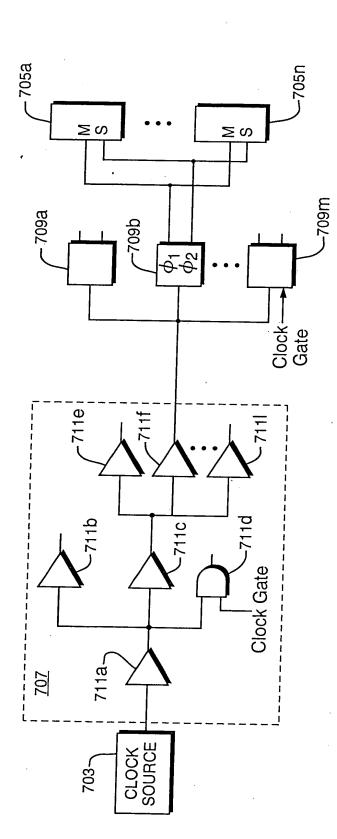
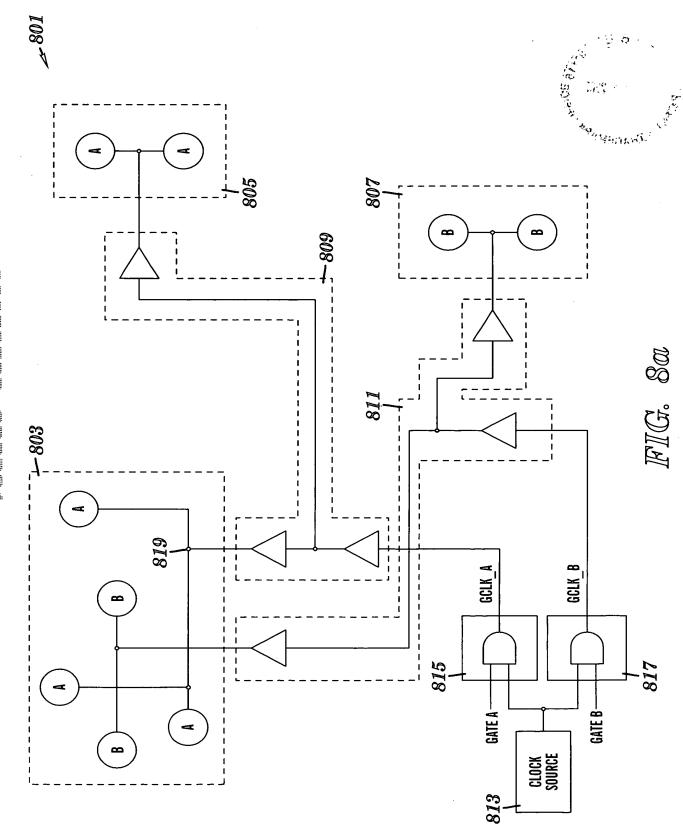
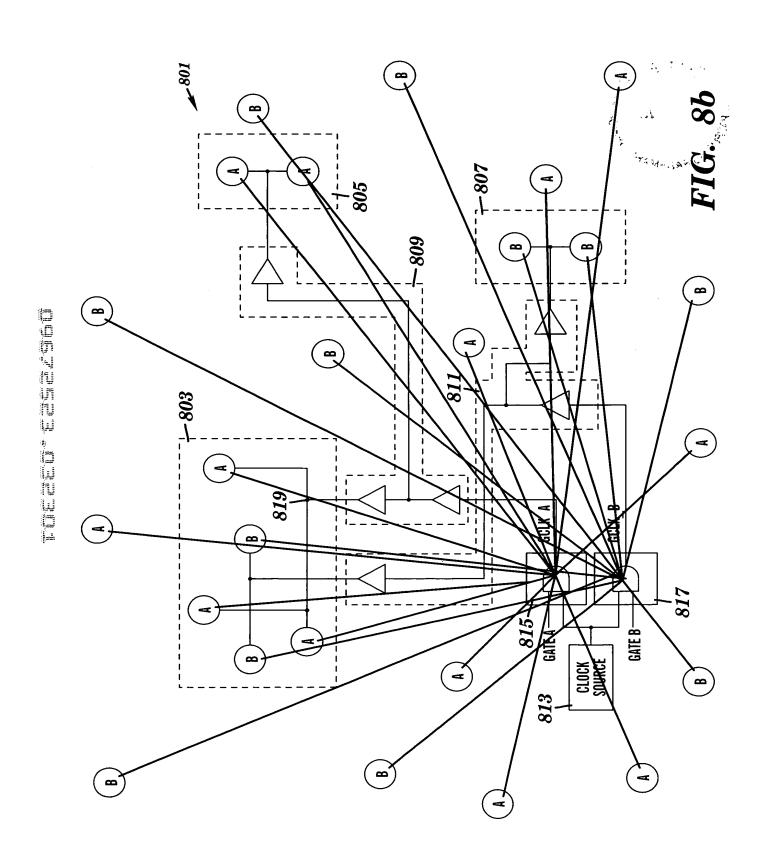


FIG. 7





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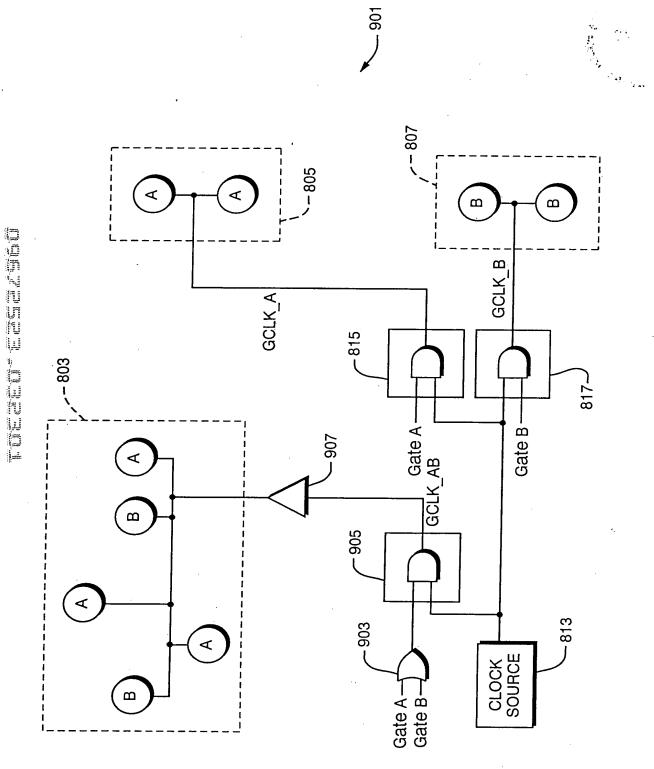


FIG. 9

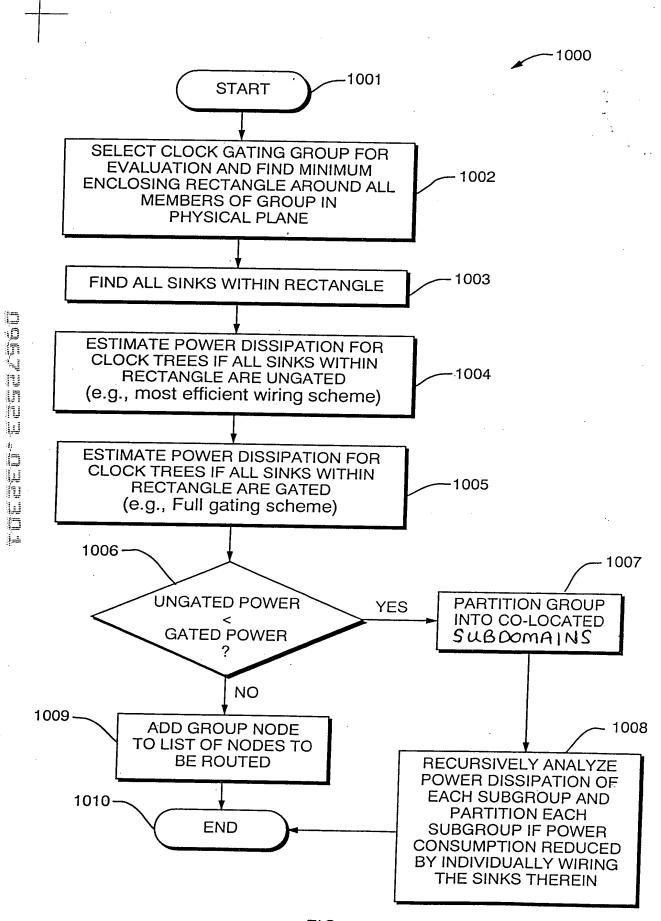
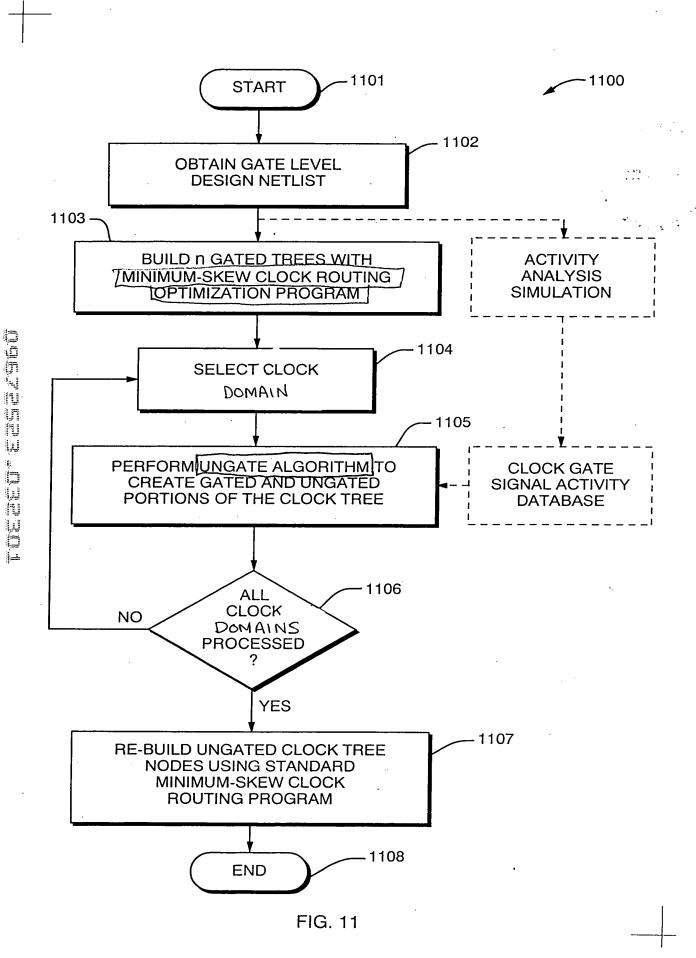


FIG. 10



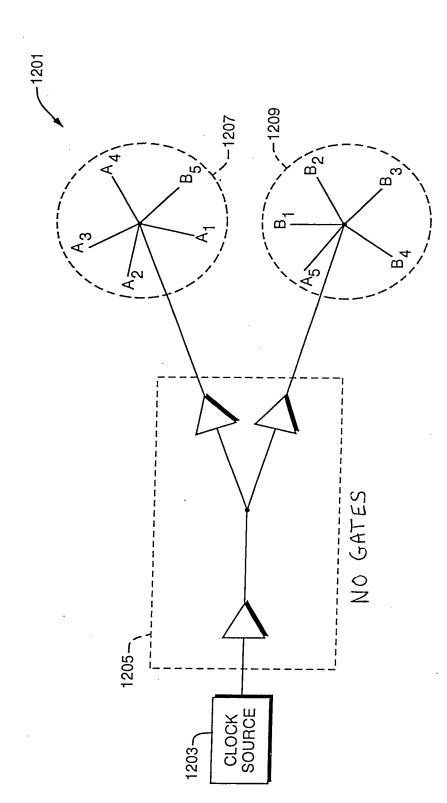


FIG. 12

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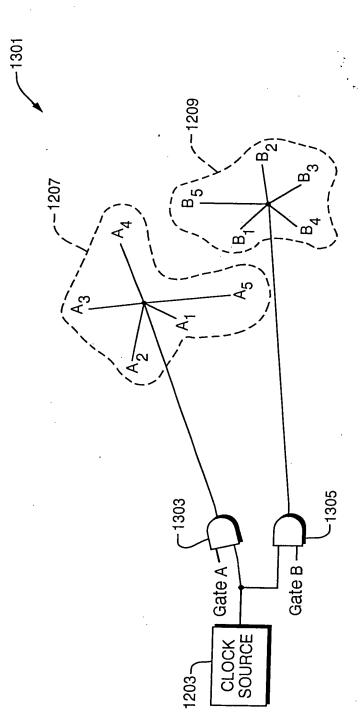


FIG. 13

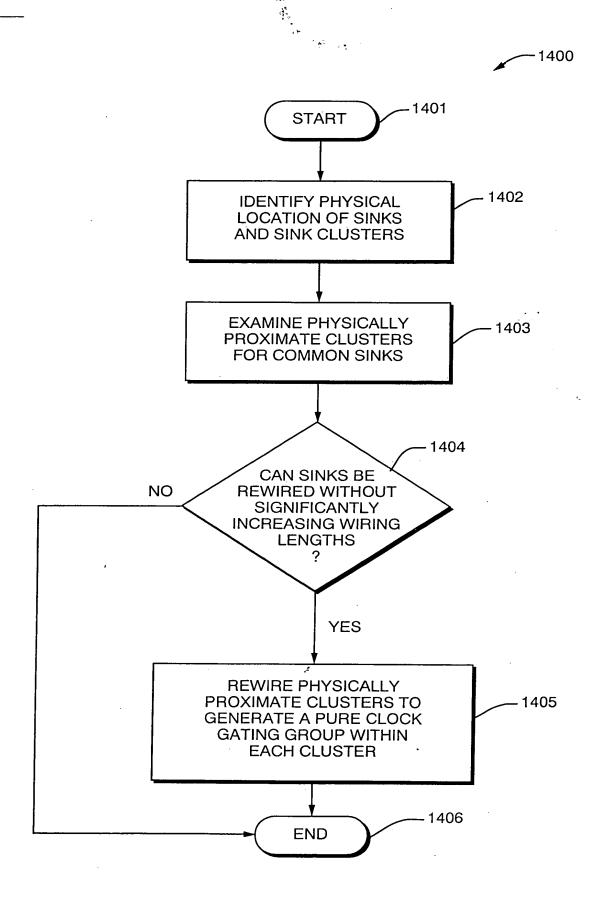


FIG. 14

DOETEDS CHEST